

# TABLAS DE ERRORES POR MUESTRA Y MARCADOR

Ver leyenda explicativa al final del anexo.

Muestra 1							
	alelo->stutter	stutter->alelo	drop-out	drop-in	error transcr.	Total Labs	Total errores
D8S1179		3	4			6	7
D21S11		2				2	2
D7S820					1	1	1
CSF1PO		1				1	1
D3S1358							0
TH01			4	1		5	5
D13S317							0
D16S539	1		3			3	4
D2S1338							0
D19S433	1					1	1
vWA		1				1	1
TPOX							0
D18S51		2				2	2
D5S818			2			2	2
FGA			2			2	2
Penta E	22	1	1			22	24
Penta D	7		24			24	31
	31	10	40	1	1		83

Muestra 2							
	alelo->stutter	stutter->alelo	drop-out	drop-in	error transcr.	Total Labs	Total errores
D8S1179		9				5	9
D21S11	4		4	1		7	9
D7S820		4	1			5	5
CSF1PO		11	4			13	15
D3S1358	7		2			8	9
TH01			3			3	3
D13S317	2	5				7	7
D16S539		10	1			10	11
D2S1338		9	7			15	16
D19S433	4					4	4
vWA		4				4	4
TPOX	2					1	2
D18S51		12	9	1		19	22
D5S818	2	5	2			8	9
FGA			13			10	13
Penta E			24			24	24
Penta D							
	21	69	70	2	0		162

### Muestra 3

	alelo->stutter	stutter->alelo	drop-out	drop-in	error transcr.	Total Labs	Total errores
D8S1179							0
D21S11							0
D7S820							0
CSF1PO							0
D3S1358		1				1	1
TH01							0
D13S317							0
D16S539		1				1	1
D2S1338					1	1	1
D19S433							0
vWA							0
TPOX							0
D18S51							0
D5S818		3	1			2	4
FGA							0
Penta E							0
Penta D							0
	0	5	1	0	1		7

### Muestra 4

	alelo->stutter	stutter->alelo	drop-out	drop-in	error transcr.	Total Labs	Total errores
D8S1179							0
D21S11			2			2	2
D7S820							0
CSF1PO							0
D3S1358		3	1			3	4
TH01			1			1	1
D13S317							0
D16S539							0
D2S1338							0
D19S433			1			1	1
vWA							0
TPOX							0
D18S51							0
D5S818		1				1	1
FGA							0
Penta E			1			1	1
Penta D							0
	0	4	6	0	0		10

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<b>Drop in</b>	Generar aparición de un alelo NO REAL
<b>Drop out</b>	Generar una pérdida de alelo REAL
<b>alelo -&gt; stutter</b>	Confunde un alelo real con un stutter
<b>stutter -&gt; alelo</b>	Confunde un stutter real con un alelo
<b>error transcripción</b>	

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